

## ABSTRACT:

An arbiter 15 for accessing a single-port RAM 13 without the need to use a hand-shaking protocol is proposed. This allows simultaneous read and write accesses to a single-port RAM 13. All write accesses are delayed so that the arbiter 15 can detect if there is a simultaneous read access. If there is a read access, the read access is delayed until the write  
5 access is completed.

(Fig. 1)